

EXPRESS MAIL LABEL NO. EV314046093US

DATED: 03 SEPTEMBER 2003

ATTORNEY DOCKET: TRAM 1002-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventor:

Shahram Abdollahi-Alibeik et al.

Application No.: _____

Filed: **03 September 2003 (herewith)**

Title: **Delay Line and Output Clock Generator Using
Same**

Group Art Unit: **unknown**

Examiner: **unknown**

Customer No. 22470

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98.


This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed within three months of the filing date of an application other than a continued prosecution application under 37 C.F.R. 1.53(d).

Fee Authorization. The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (TRAM 1002-1). A duplicate copy of this authorization is enclosed.

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Date: 3 Sept 2003

By: 
Mark A. Haynes, Reg. No. 30,846

HAYNES BEFFEL & WOLFELD LLP

P.O. Box 366

Half Moon Bay, CA 94019

Telephone: (650) 712-0340

Facsimile: (650) 712-0263

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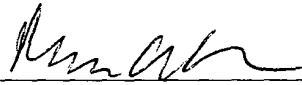
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
Sheet		1	of	3	Application Number Filing Date First Named Inventor Group Art Unit Examiner Name Attorney Docket Number
				03 September 2003 (herewith)	Shahram Abdollahi-Alibeik et al.
					TRAM 1002-1

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	A1	5,243,227		Gutierrez, Jr., et al.	09-07-1993	
	A2	5,298,866		Kaplinsky	03-29-1994	
	A3	5,424,980		Vinal	06-13-1995	
	A4	5,432,747		Fuller et al.	07-11-1995	
	A5	5,561,692		Maitland et al.	10-01-1996	
	A6	5,596,539		Passow et al.	01-21-1997	
	A7	5,703,815		Kuhara et al.	12-30-1997	
	A8	5,805,872		Bannon	09-08-1998	
	A9	5,831,924		Nitta et al.	11-03-1998	
	A10	5,999,482		Kornachuk et al.	12-07-1999	
	A11	6,025,744		Bertolet et al.	02-15-2000	
	A12	6,081,462		Lee	06-27-2000	
	A13	6,084,805		Pawlowski	07-04-2000	
	A14	6,154,417		Kim	11-28-2000	
	A15	6,157,231		Wasson	12-05-2000	
	A16	6,160,754		Suh	12-12-2000	
	A17	6,205,086		Hanzawa et al.	03-20-2001	
	A18	6,212,117		Shin et al.	04-03-2001	
	A19	6,229,161		Nemati et al.	05-08-2001	
	A20	6,243,784		Anderson et al.	06-05-2001	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	B1		WO 02/082453		T-RAM, Inc.	10-17-2002		
	B2		WO 02/082504		T-RAM, Inc.	10-17-2002		
	B3							
	B4							
	B5							
	B6							
	B7							
	B8							
	B9							
	B10							

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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Sheet		2	of	3	Application Number	
					Filing Date	
					03 September 2003 (herewith)	
					First Named Inventor	
					Shahram Abdollahi-Alibeik et al.	
					Group Art Unit	
					Examiner Name	
					Attorney Docket Number	
					TRAM 1002-1	

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		Number	Kind Code ² (if known)			
	A21	6,278,637		Kawaguchi	08-21-2001	
	A22	6,318,707		Hara et al.	11-20-2001	
	A23	6,337,830		Faue	01-08-2002	
	A24	6,337,832		Ooishi et al.	01-08-2002	
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	A27	6,430,075		Morgan et al.	08-06-2002	
	A28	6,459,652		Lee et al.	10-01-2002	
	A29	6,462,359		Nemati et al.	10-08-2002	
	A30	6,490,206		Kwon et al.	12-03-2002	
	A31	2001/0043482		Takeyama et al.	11-22-2001	
	A32	2002/0190265		Hsu et al.	12-19-2002	
	A33	2003/0002355		Janzen et al.	01-02-2003	
	A34					
	A35					
	A36					
	A37					
	A38					
	A39					
	A40					

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	B11							
	B12							
	B13							
	B14							
	B15							
	B16							
	B17							
	B18							
	B19							
	B20							

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<div style="text-align: right;">Substitute for form 1449B/PTO</div> INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>			Complete if Known		
			Application Number		
			Filing Date	03 September 2003 (herewith)	
			First Named Inventor	Shahram Abdollahi-Alibeik et al.	
			Group Art Unit		
			Examiner Name		
Sheet	3	of	3	Attorney Docket Number	TRAM 1002-1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C1	NEMATI, FARID et al., "A Novel High Density, Low Voltage SRAM Cell with a Vertical NDR Device," T-Ram White Paper, VLSI (1998) 2 pages, http://www.t-ram.com/technology/about/vlsi98.pdf	
	C2	NEMATI, FARID et al., "A Novel Thyristor-Based SRAM Cell (T-RAM) for High-Speed, Low-Voltage, Giga-scale Memories," T-Ram White Paper, IEDM (1999), 4 pages http://www.t-ram.com/technology/about/iedm99.pdf	

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